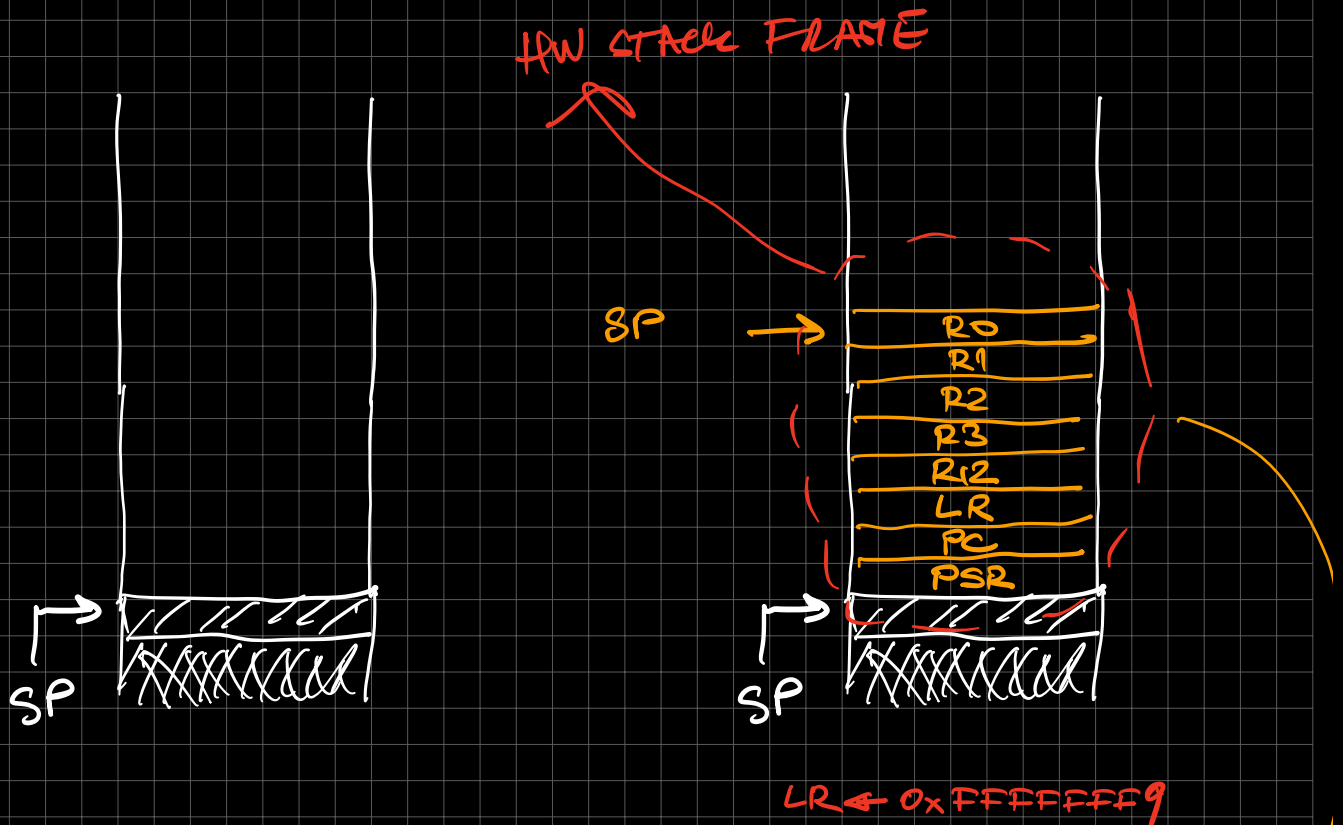


# Task itching

Pred prekinitvijo:

Stacky operaciji:



Sedaj se itrova PSP


Ob izstopu iz PSP (bx LR ⇒ PC ← LR)

se naredi DESTABILIZACIJSKE OPERACIJE:  
pobere se PR register s sleden

Če je ob destacku operaciji ima SP enako vredost kot ob stacku operaciji, se vrne v program, kjer smo se preden.

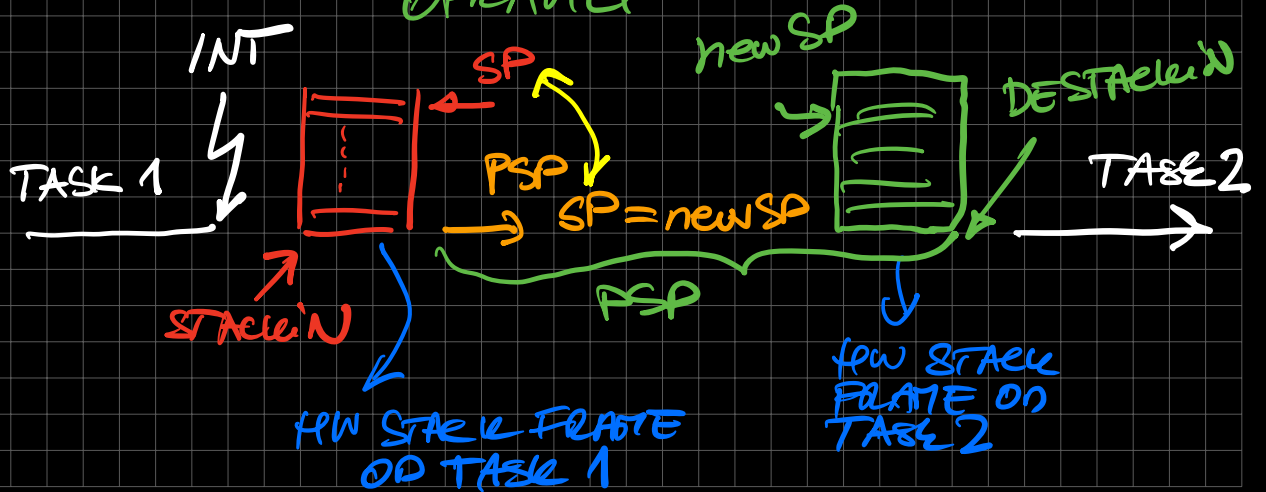


Hlal! Če pa SP ob <sup>DE</sup>stacku operaciji nima enake vredosti, kot po stacku operaciji, potem se vrne na nek drug kraj.

IDEJA : preden naredimo destacku operacijo, zamenjamo SP tako, da bo kazal na nek drug del STACK FRAME

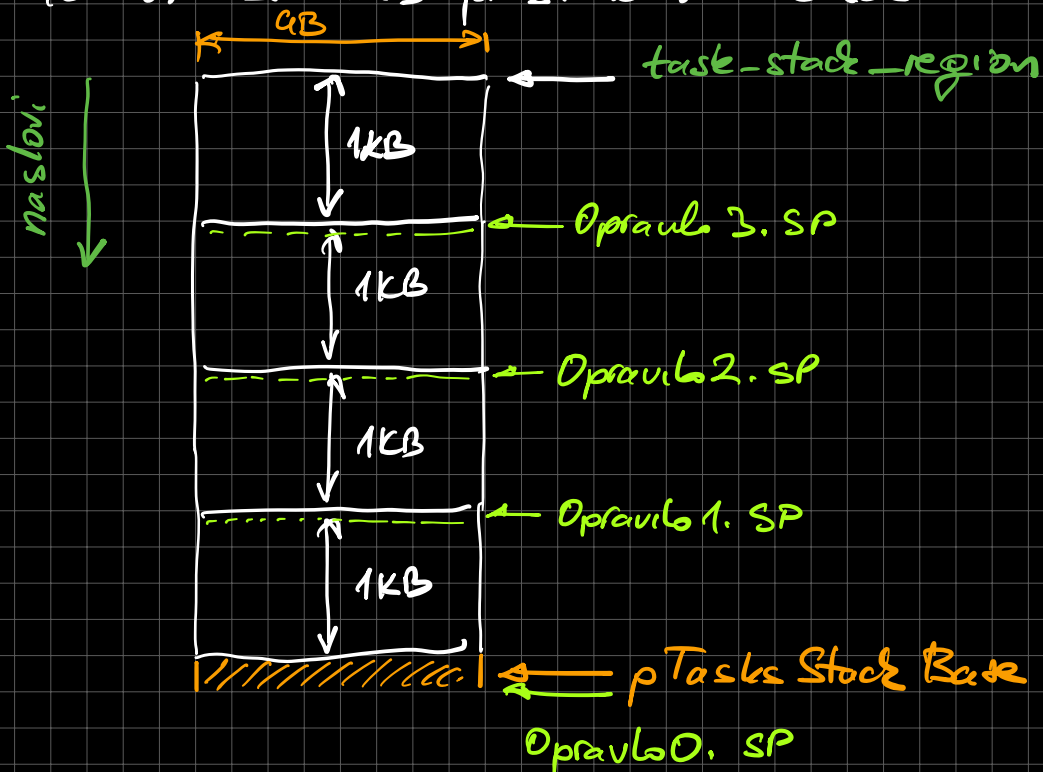


TAKO SE BOMO VRNILI  
DRUGAM IN DE-FACTO ZAHYENJATI  
OPRAVILI



1. Vsaka TASK mora imeti ločen sklop

Predpostavimo 4 opravila, vsako opravilo ima 1KB oz. 256 x 64B prostora za sklop



Za vsako opravilo bomo imeli eno strukturo, ki bo hkrati "sklepi" opravilo → vse tole, kar se ne hрани na HW st. frame

začetna vredost PC

trenutna vredost SP

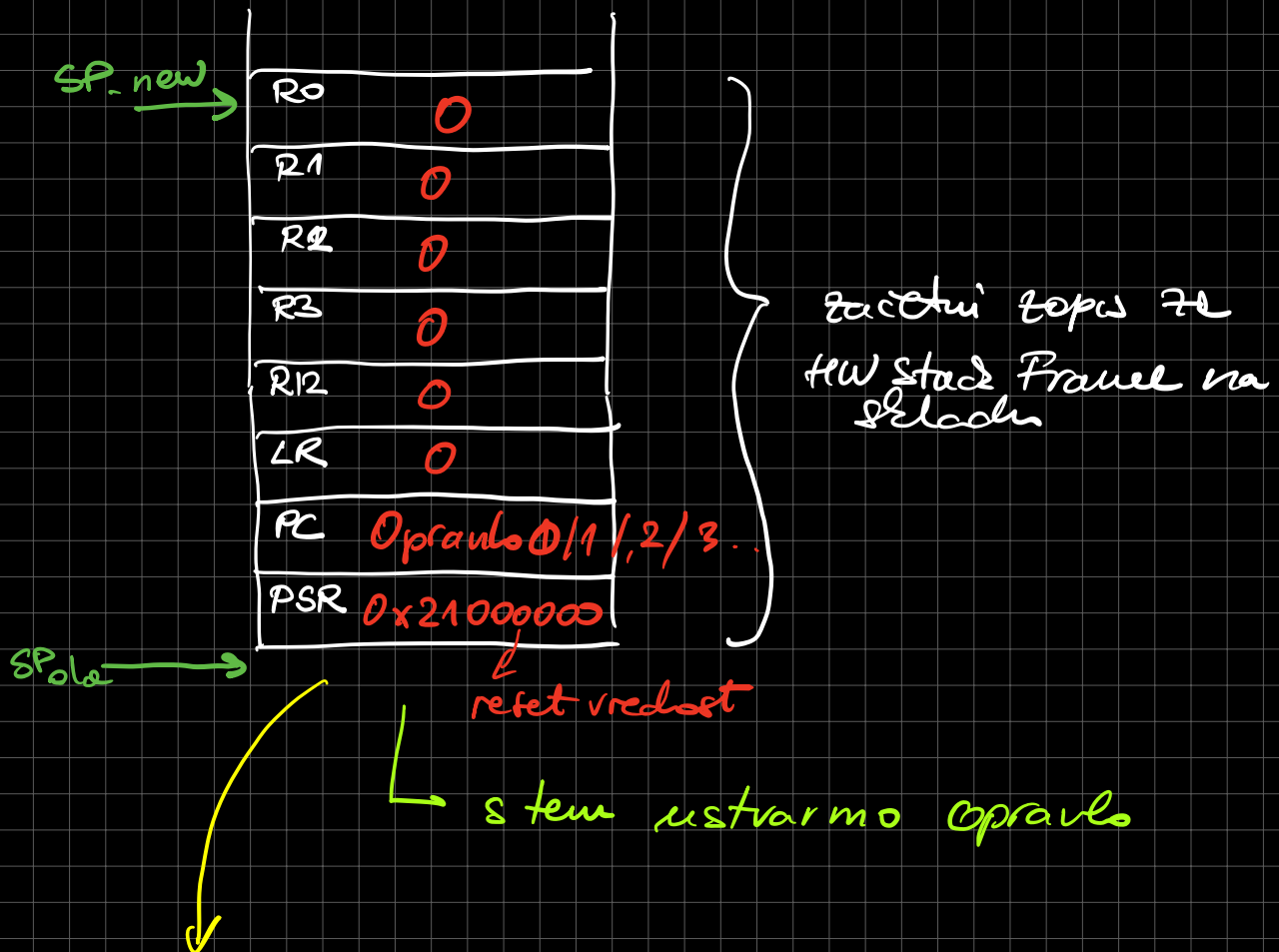
```

1
2= /*
3   Task state structure.
4   In our simple scheduler, the task is determined only by its stack pointer and
5   the start address (i.e. pointer to a function that implements the task)
6   but in general, the structure should contain a task's state variable (flags)
7   (e.g. RUNNING, STOPPED, ACTIVE, ....)
8
9   Pa3cio Bulic, 9.11.2020
10  */
11= typedef struct {
12     void* sp;           // task's stack pointer
13     void (*pTask)(void); // start address of the task
14     int flags;
15 } task_table_t;
16

```

2. Za vsako opravilo moramo na sleden  
sami ustvariti "začetni" zapis  
HW stack Frame

↓  
Ustvarimo moramo zapolniti prvih  
8 32-bitnih besed na sledu:



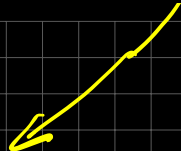
Da mi bo lažje tole zapisati na sledu,  
bom naredil abstrakcijo tega zapisa  
& eno podatkovno strukturo:

```

/*
Hardware Stack Frame structure.

Pa3cio Bulic, 9.11.2020
*/
typedef struct {
uint32_t r0;
uint32_t r1;
uint32_t r2;
uint32_t r3;
uint32_t r12;
uint32_t lr;
uint32_t pc;
uint32_t psr;
} hw_stack_frame_t;

```



za ustrojbu opravilo (= ustrojbu) zahteva zaplo na delu):

```

void create_task (int task_id, void* task_stack_base_address, void (*pTask)(void)) {
hw_stack_frame_t* ptask_hw_frame;
/* set the start address of the HW frame structure */
ptask_hw_frame = (hw_stack_frame_t *) ((uint8_t *)task_stack_base_address - sizeof(hw_stack_frame_t));

/* populate the HW stack frame with initial values : */
/* NOTE: HW stack for Task0 (main()) in ignored as main() uses MAIN STACK */
ptask_hw_frame -> r0 = 0;
ptask_hw_frame -> r1 = 0;
ptask_hw_frame -> r2 = 0;
ptask_hw_frame -> r3 = 0;
ptask_hw_frame -> r12 = 0;
ptask_hw_frame -> lr = 0; // in our simple RTOS the tasks never finish so there is no need to delete the task
ptask_hw_frame -> pc = (uint32_t)pTask;
ptask_hw_frame -> psr = 0x01000000;

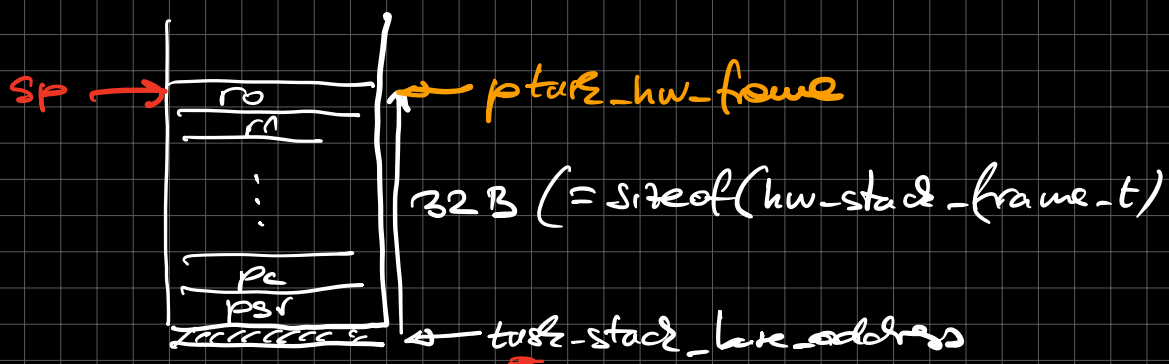
// make room for SW stack frame and set the task's stack pointer:
task_table[task_id].sp = (void *) ( (uint8_t *)task_stack_base_address
- sizeof(hw_stack_frame_t)
- sizeof(hw_stack_frame_t) );
}

```

neinca; blivna; lezalec na strukturo

32 B

CAST!  
= (ptask\_hw\_frame



Sedq' zomyo frukyo polidens tole hot Goll  
fo opial ?

```
void init_tasks (void) {  
  
    int i;  
  
    /* Task schedule Opialo 0  
    sets tasks in the schedule: */  
    task_table[0].pTask = &idle; /* this value will be overwritten after first SysTick exception...*/  
    task_table[1].pTask = &task1; Opialo 1  
    task_table[2].pTask = &idle; Opialo 2  
    task_table[3].pTask = &task1; Opialo 3  
    task_table[4].pTask = &idle;  
    task_table[5].pTask = &task3;  
    task_table[6].pTask = &task1;  
    task_table[7].pTask = &idle;  
    task_table[8].pTask = &task1;  
    task_table[9].pTask = &task2;  
  
    /* Create tasks...*/  
    for (i = 0; i < MAX_TASKS; i++) {  
        create_task (i, (uint8_t *)pTasksStackBase - i*sizeof(uint32_t)*TASKS_STACK_SIZE, task_table[i].pTask);  
    }  
  
    /* PSP has not been set until now, so set PSP to point to  
    the top of stack of the first interrupted task (Task 0): */  
    write_process_stack_pointer(task_table[0].sp); nasledny c  
}
```

```
static inline void write_process_stack_pointer(void* ptr) {  
    asm volatile ( "MSR PSP, %0\n\t" : : "r" (ptr) );  
}
```