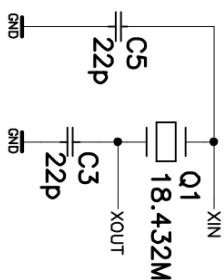


I/O

179	PA0/SPI0_MISO	179	PA0/SPI0_MISO/MCDB0
180	PA1/SPI0_MOSI	180	PA1/SPI0_MOSI/MCCDB
181	PA2/SPI0_SPCK	181	PA2/SPI0_SPCK
182	PA3/SPI0_NPCS0	182	PA3/SPI0_NPCS0/MCDB3
183	PA4	183	PA4/RTS2/MCDB2
184	PA5	184	PA5/CTS2/MCDB1
185	PA6/MCDA0	185	PA6/MCDA0
186	PA7/MCCDA	186	PA7/MCCDA
189	PAB/MCCK	189	PAB/MCCK
190	PA9/MCDA1	190	PA9/MCDA1
191	PA10/MCDA2	191	PA10/MCDA2/ETX2
192	PA11/MCDA3	192	PA11/MCDA3/ETX3
193	PA12/ETX0	193	PA12/ETX0
194	PA13/ETX1	194	PA13/ETX1
195	PA14/ERX0	195	PA14/ERX0
196	PA15/ERX1	196	PA15/ERX1
197	PA16/ETXEN	197	PA16/ETXEN
198	PA17/ERXDV	198	PA17/ERXDV
201	PA18/ERXER	201	PA18/ERXER
202	CLK50MHZ	202	PA19/ETXCK
205	PA20/EMDC	205	PA20/EMDC
206	PA21/EMDIO	206	PA21/EMDIO
207	PA22/ADTRG/ETXER	207	PA22/ADTRG/ETXER
208	PA23	208	PA23/TWD/ETX2
1	PA24	1	PA24/TWCK/ETX3
2	PA25	2	PA25/TCLK0/ERX2
3	PA26	3	PA26/TIOA0/ERX3
4	PA27	4	PA27/TIOA1/ERXCK
7	PA28	7	PA28/TIOA2/ECRS
8	PA29	8	PA29/SCK1/ECOL
9	PB0	9	PB0/SPI1_MISO/TIOA3
10	PB1	10	PB1/SPI1_MOSI/TIOB3
11	PB2	11	PB2/SPI1_SPCK/TIOA4
12	PB3	12	PB3/SPI1_NPCS0/TIOA5
15	PB4	15	PB4/TXD0
16	PB5	16	PB5/RXD0
17	PB6	17	PB6/TXD1/TCLK1
18	PB7	18	PB7/RXD1/TCLK2
19	PB8	19	PB8/TXD2
20	PB9	20	PB9/RXD2
161	PB10	161	PB10/TXD3/ISI_D8
162	PB11	162	PB11/RXD3/ISI_D9
21	PB14/DRXD	21	PB14/DRXD
22	PB15/DTXD	22	PB15/DTXD
23	PB16	23	PB16/TK0/TCLK3
26	PB17	26	PB17/TF0/TCLK4
27	PB18	27	PB18/TD0/TIOB4
28	PB19	28	PB19/RD0/TIOB5
163	PB20	163	PB20/RK0/ISI_D0
164	PB21	164	PB21/RF0/ISI_D1
165	PB22	165	PB22/DSR0/ISI_D2
166	PB23	166	PB23/DCD0/ISI_D3
167	PB24	167	PB24/DTR0/ISI_D4
168	PB25	168	PB25/RI0/ISI_D5
171	PB26	171	PB26/RTS0/ISI_D6
172	PB27	172	PB27/CTS0/ISI_D7
175	PB28	175	PB28/RTS1/ISI_PCK
176	PB29	176	PB29/CTS1/ISI_VSYNC
177	PB30	177	PB30/PCK0/ISI_HSYNC
178	PB31	178	PB31/PCK1/ISI_MCK
158	PC0	158	PC0/SCK3/AD0
159	PC1	159	PC1/PCK0/AD1
62	PC4	62	PC4/A23/SPI1_NPCS2
67	PC5	67	PC5/A24/SPI1_NPCS1
63	PC6/CFCE1	63	PC6/TIOB2/CFCE1
64	PC7/CFCE2	64	PC7/TIOB1/CFCE2
61	PC8/CFCS0	61	PC8/NCS4/CFCS0/RTS3
60	PC9	60	PC9/NCS5/CFCS1/TIOB0
58	PC10/A25/CFRNW	58	PC10/A25/CFRNW/CTS3
57	PC11	57	PC11/NCS2/SPI0_NPCS1
56	PC13	56	PC13/FIQ/CS6
59	PC14	59	PC14/CS3/NANDC/IRQ
127	PC15/NWAIT	127	PC15/WAIT/IRQ1
128	D16	128	PC16/D16/SPI0_NPCS2
129	D17	129	PC17/D17/SPI0_NPCS3



## 41. AT91SAM9260 Electrical Characteristics

### 41.1 Absolute Maximum Ratings

**Table 41-1.** Absolute Maximum Ratings\*

Operating Temperature (Industrial).....	-40°C to +85°C
Storage Temperature.....	-60°C to +150°C
Voltage on Input Pins with Respect to Ground..	-0.3V to VDDIO+0.3V(+4V max)
Maximum Operating Voltage (VDDCORE, VDDPLL and VDDBU).....	2.0V
Maximum Operating Voltage (VDDIOM and VDDIOP) .....	4.0V
Total DC Output Current on all I/O lines .....	350 mA

**Table 41-3.** Power Consumption for Different Modes<sup>(1)</sup>

Mode	Conditions	Consumption	Unit
Active	ARM Core clock is 180 MHz. MCK is 90 MHz. All peripheral clocks activated. onto AMP2	130	mA
Idle	Idle state, waiting an interrupt. All peripheral clocks de-activated. onto AMP2	17	mA
Ultra low power	ARM Core clock is 500 Hz. All peripheral clocks de-activated. onto AMP2	600	µA
Backup	Device only V <sub>DDBU</sub> powered onto AMP1	5	µA

### 41.2 DC Characteristics

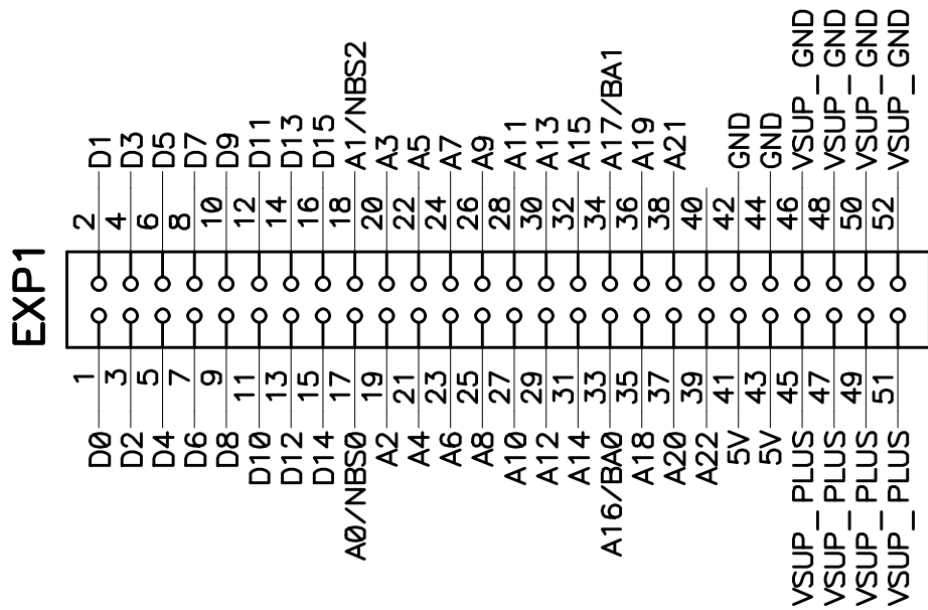
The following characteristics are applicable to the operating temperature range: T<sub>A</sub> = -40°C to 85°C, unless otherwise specified.

**Table 41-2.** DC Characteristics

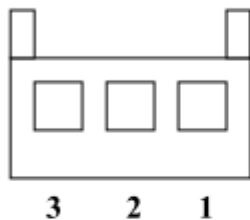
Symbol	Parameter	Conditions	Min	Typ	Max	Units
V <sub>VDDCORE</sub>	DC Supply Core		1.65	1.8	1.95	V
V <sub>VDDBU</sub>	DC Supply Backup		1.65	1.8	1.95	V
V <sub>VDDPLL</sub>	DC Supply PLL		1.65	1.8	1.95	V
V <sub>VDDIOM</sub>	DC Supply Memory I/Os	selectable by software	1.65/3.0	1.8/3.3	1.95/3.6	V
V <sub>VDDIOP0</sub>	DC Supply Peripheral I/Os		3.0	3.3	3.6	V
V <sub>VDDIOP1</sub>	DC Supply Peripheral I/Os		1.65	1.8/2.5/3.3	3.6	V
V <sub>VDDANA</sub>	DC Supply Analog		3.0	3.3	3.6	V
V <sub>IL</sub>	Input Low-level Voltage	V <sub>VDDIO</sub> from 3.0V to 3.6V	-0.3		0.8	V
		V <sub>VDDIO</sub> from 1.65V to 1.95V	-0.3		0.3 x V <sub>VDDIO</sub>	V
V <sub>IH</sub>	Input High-level Voltage	V <sub>VDDIO</sub> from 3.0V to 3.6V	2.0		V <sub>VDDIO</sub> +0.3V	V
		V <sub>VDDIO</sub> from 1.65V to 1.95V	0.7 x V <sub>VDDIO</sub>		V <sub>VDDIO</sub> +0.3V	V
V <sub>OL</sub>	Output Low-level Voltage	I <sub>O</sub> Max, V <sub>VDDIO</sub> from 3.0V to 3.6V			0.4	V
		CMOS (I <sub>O</sub> <0.3 mA) V <sub>VDDIO</sub> from 1.65V to 1.95V			0.1	V
		TTL (I <sub>O</sub> Max) V <sub>VDDIO</sub> from 1.65V to 1.95V			0.4	V
V <sub>OH</sub>	Output High-level Voltage	I <sub>O</sub> Max, V <sub>VDDIO</sub> from 3.0V to 3.6V	V <sub>VDDIO</sub> - 0.4			V
		CMOS (I <sub>O</sub> <0.3 mA) V <sub>VDDIO</sub> from 1.65V to 1.95V	V <sub>VDDIO</sub> - 0.1			V
		TTL (I <sub>O</sub> Max) V <sub>VDDIO</sub> from 1.65V to 1.95V	V <sub>VDDIO</sub> - 0.4			

**Table 41-2.** DC Characteristics (Continued)

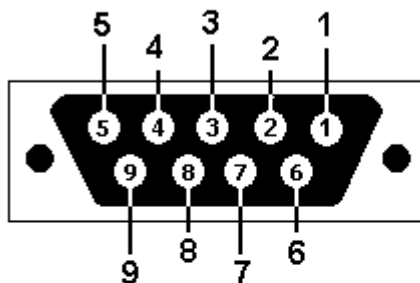
R <sub>PULLUP</sub>	Pull-up Resistance	PA0-PA31 PB0-PB31 PC0-PC3 NTRST and NRST	67	100	180	kOhm
		PC4 - PC31 V <sub>VDDIOM</sub> in 1.8V range	240		1000	
		PC4 - PC31 V <sub>VDDIOM</sub> in 3.3V range	120		350	
I <sub>O</sub>	Output Current	PA0-PA31 PB0-PB31 PC0-PC3			16	mA
		PC4 - PC31 in 3.3V range			2	
		PC4 - PC31 in 1.8V range			4	
I <sub>SC</sub>	Static Current	On V <sub>VDDCORE</sub> = 1.8V, MCK = 0 Hz, excluding POR	T <sub>A</sub> =25°C		500	µA
		All inputs driven TMS, TDI, TCK, NRST = 1	T <sub>A</sub> =85°C		5000	
		On V <sub>VDDBU</sub> = 1.8V, Logic cells consumption, excluding POR	T <sub>A</sub> =25°C		2	µA
		All inputs driven WKUP = 0	T <sub>A</sub> =85°C		20	



### NAČRT POVEZAV ZA RS232 KABEL



CRIMP



DB9 ženski

CRIMP		DB9	
Nožica	Ime signala	Nožica	Ime signala
1	TD	2	RD
2	GND	5	GND
3	RD	3	TD